

Operational Semantics, Part 2

Martin Kellogg

Agenda

- Review: basics of operational semantics
- Operational semantics of Cool
- (if time): introduction to static analysis
 - further if time: get into abstract interpretation

Agenda

- **Review: basics of operational semantics**
- Operational semantics of Cool
- (if time): introduction to static analysis
 - further if time: get into abstract interpretation

Review: High-level Idea of Op. Sem.

- An *operational semantics* is a precise way of specifying how to evaluate a program.
 - A *formal semantics* tells you what each expression means.

Review: High-level Idea of Op. Sem.

- An *operational semantics* is a precise way of specifying how to evaluate a program.
 - A *formal semantics* tells you what each expression means.
- Meaning depends on *context*: a *variable environment* will map variables to memory locations and a *store* will map memory locations to values.

Review: High-level Idea of Op. Sem.

- An **operational semantics** is a precise way of specifying how to evaluate a program.
 - A **formal semantics** tells you what each expression means.
- Meaning depends on **context**: a **variable environment** will map variables to memory locations and a **store** will map memory locations to values.
 - **environment**: names \rightarrow (abstract) locations
 - **store**: (abstract) locations \rightarrow values

Review: High-level Idea of Op. Sem.

- An **operational semantics** is a precise way of specifying how to evaluate a program.
 - A **formal semantics** tells you what each expression means.
- Meaning depends on **context**: a **variable environment** will map variables to memory locations and a **store** will map memory locations to values.
 - **environment**: names -> (abstract) locations
 - **store**: (abstract) locations -> values
- We will specify Cool's semantics via **logical rules of inference** that specify how to compute the “next step” in the program

Review: Operational Rules of Cool

- The *evaluation judgment* is

$$\text{so, } \mathbf{E}, \mathbf{S} \vdash \mathbf{e} : \mathbf{v}, \mathbf{S}'$$

- read as:

Review: Operational Rules of Cool

- The *evaluation judgment* is

$$\text{so}, \mathbf{E}, \mathbf{S} \vdash \mathbf{e} : \mathbf{v}, \mathbf{S}'$$

- read as:
 - Given **so**, the current value of the **self** object;
 - and **E**, the current variable environment;
 - and **S**, the current store;
 - and if the evaluation of **e** *terminates*, then
 - the returned value is **v**
 - and the new store is **S'**

Review: Operational Semantics for Base Values

$\text{so}, E, S \vdash \text{true} : \text{Bool}(\text{true}), S$

$\text{so}, E, S \vdash \text{false} : \text{Bool}(\text{false}), S$

i is any integer literal

$\text{so}, E, S \vdash i : \text{Int}(i), S$

s is any string literal
n is the length of s

$\text{so}, E, S \vdash s : \text{String}(s, n), S$

Review: Operational Semantics for Variables

$$\frac{E(id) = l_{id} \quad S(l_{id}) = v}{\text{so, } E, S \vdash id : v, S}$$

- Note the **double lookup** of variables
 - First from name to location (at compile time)
 - Then from location to value (at run time)

Review: Operational Semantics for Assignments

$$\frac{\text{so, } \mathbf{E}, \mathbf{S} \vdash \mathbf{e} : \mathbf{v}, \mathbf{S}_1 \quad \mathbf{E}(\mathbf{id}) = \mathbf{l}_{\mathbf{id}} \quad \mathbf{S}_2 = \mathbf{S}_1[\mathbf{v}/\mathbf{l}_{\mathbf{id}}]}{\text{so, } \mathbf{E}, \mathbf{S} \vdash \mathbf{id} \leftarrow \mathbf{e} : \mathbf{v}, \mathbf{S}_2}$$

- A three-step process:
 - Evaluate the right-hand side to get a value \mathbf{v} and a new store \mathbf{S}_1
 - Fetch the location of the assigned variable
 - The result is the value \mathbf{v} and an updated store \mathbf{S}_2

Operational Semantics for Conditionals

Operational Semantics for Conditionals

so, $E, S \vdash e_1 : \text{Bool}(\text{true}), S_1$

so, $E, S_1 \vdash e_2 : v, S_2$

so, $E, S \vdash \text{if } e_1 \text{ then } e_2 \text{ else } e_3 \text{ fi} : v, S_2$

Operational Semantics for Conditionals

$$\text{so, } \mathbf{E}, \mathbf{S} \vdash e_1 : \text{Bool}(\text{true}), \mathbf{S}_1$$
$$\text{so, } \mathbf{E}, \mathbf{S}_1 \vdash e_2 : v, \mathbf{S}_2$$

$$\text{so, } \mathbf{E}, \mathbf{S} \vdash \text{if } e_1 \text{ then } e_2 \text{ else } e_3 \text{ fi} : v, \mathbf{S}_2$$

- The “**threading**” of the store enforces an evaluation sequence:

Operational Semantics for Conditionals

$$\text{so, } \mathbf{E}, \mathbf{S} \vdash e_1 : \text{Bool}(\text{true}), \mathbf{S}_1$$
$$\text{so, } \mathbf{E}, \mathbf{S}_1 \vdash e_2 : v, \mathbf{S}_2$$

$$\text{so, } \mathbf{E}, \mathbf{S} \vdash \text{if } e_1 \text{ then } e_2 \text{ else } e_3 \text{ fi} : v, \mathbf{S}_2$$

- The “**threading**” of the store enforces an evaluation sequence:
 - e_1 must be evaluated first to produce \mathbf{S}_1

Operational Semantics for Conditionals

$$\text{so, } \mathbf{E}, \mathbf{S} \vdash \mathbf{e}_1 : \text{Bool}(\text{true}), \mathbf{S}_1$$
$$\text{so, } \mathbf{E}, \mathbf{S}_1 \vdash \mathbf{e}_2 : \mathbf{v}, \mathbf{S}_2$$

$$\text{so, } \mathbf{E}, \mathbf{S} \vdash \text{if } \mathbf{e}_1 \text{ then } \mathbf{e}_2 \text{ else } \mathbf{e}_3 \text{ fi} : \mathbf{v}, \mathbf{S}_2$$

- The “**threading**” of the store enforces an evaluation sequence:
 - \mathbf{e}_1 must be evaluated first to produce \mathbf{S}_1
 - then \mathbf{e}_2 can be evaluated.

Operational Semantics for Conditionals

$$\text{so, } \mathbf{E}, \mathbf{S} \vdash \mathbf{e}_1 : \text{Bool}(\text{true}), \mathbf{S}_1$$
$$\text{so, } \mathbf{E}, \mathbf{S}_1 \vdash \mathbf{e}_2 : \mathbf{v}, \mathbf{S}_2$$

$$\text{so, } \mathbf{E}, \mathbf{S} \vdash \text{if } \mathbf{e}_1 \text{ then } \mathbf{e}_2 \text{ else } \mathbf{e}_3 \text{ fi} : \mathbf{v}, \mathbf{S}_2$$

- The “**threading**” of the store enforces an evaluation sequence:
 - \mathbf{e}_1 must be evaluated first to produce \mathbf{S}_1
 - then \mathbf{e}_2 can be evaluated.
- The result of evaluating \mathbf{e}_1 is a boolean object
 - the type rules ensure this

Operational Semantics for Conditionals

$$\frac{\begin{array}{c} \text{so, } \mathbf{E}, \mathbf{S} \vdash \mathbf{e}_1 : \text{Bool}(\text{true}), \mathbf{S}_1 \\ \text{so, } \mathbf{E}, \mathbf{S}_1 \vdash \mathbf{e}_2 : \mathbf{v}, \mathbf{S}_2 \end{array}}{\text{so, } \mathbf{E}, \mathbf{S} \vdash \text{if } \mathbf{e}_1 \text{ then } \mathbf{e}_2 \text{ else } \mathbf{e}_3 \text{ fi} : \mathbf{v}, \mathbf{S}_2}$$

- The “**threading**” of the store enforces an evaluation sequence:
 - \mathbf{e}_1 must be evaluated first to produce \mathbf{S}_1
 - then \mathbf{e}_2 can be evaluated.
- The result of evaluating \mathbf{e}_1 is a boolean object
 - the type rules ensure this
 - there is another, similar, rule for **Bool(false)**

Operational Semantics for Sequences

$$\text{so}, \mathbf{E}, \mathbf{S} \vdash \mathbf{e}_1 : \mathbf{v}_1, \mathbf{S}_1$$
$$\text{so}, \mathbf{E}, \mathbf{S}_1 \vdash \mathbf{e}_2 : \mathbf{v}_2, \mathbf{S}_2$$
$$\dots$$
$$\text{so}, \mathbf{E}, \mathbf{S}_{n-1} \vdash \mathbf{e}_n : \mathbf{v}_n, \mathbf{S}_n$$

$$\text{so}, \mathbf{E}, \mathbf{S} \vdash \{ \mathbf{e}_1 ; \dots ; \mathbf{e}_n ; \} : \mathbf{v}_n, \mathbf{S}_n$$

Operational Semantics for Sequences

$$\frac{\begin{array}{c} \text{so, } \mathbf{E}, \mathbf{S} \vdash \mathbf{e}_1 : \mathbf{v}_1, \mathbf{S}_1 \\ \text{so, } \mathbf{E}, \mathbf{S}_1 \vdash \mathbf{e}_2 : \mathbf{v}_2, \mathbf{S}_2 \\ \dots \\ \text{so, } \mathbf{E}, \mathbf{S}_{n-1} \vdash \mathbf{e}_n : \mathbf{v}_n, \mathbf{S}_n \end{array}}{\text{so, } \mathbf{E}, \mathbf{S} \vdash \{ \mathbf{e}_1 ; \dots ; \mathbf{e}_n ; \} : \mathbf{v}_n, \mathbf{S}_n}$$

- Again, the threading of the store expresses the intended execution sequence

Operational Semantics for Sequences

$$\frac{\begin{array}{c} \text{so, } \mathbf{E}, \mathbf{S} \vdash \mathbf{e}_1 : \mathbf{v}_1, \mathbf{S}_1 \\ \text{so, } \mathbf{E}, \mathbf{S}_1 \vdash \mathbf{e}_2 : \mathbf{v}_2, \mathbf{S}_2 \\ \dots \\ \text{so, } \mathbf{E}, \mathbf{S}_{n-1} \vdash \mathbf{e}_n : \mathbf{v}_n, \mathbf{S}_n \end{array}}{\text{so, } \mathbf{E}, \mathbf{S} \vdash \{ \mathbf{e}_1 ; \dots ; \mathbf{e}_n ; \} : \mathbf{v}_n, \mathbf{S}_n}$$

- Again, the threading of the store expresses the intended execution sequence
- Only the **last value** is used

Operational Semantics for Sequences

$$\frac{\begin{array}{c} \text{so, } \mathbf{E}, \mathbf{S} \vdash \mathbf{e}_1 : \mathbf{v}_1, \mathbf{S}_1 \\ \text{so, } \mathbf{E}, \mathbf{S}_1 \vdash \mathbf{e}_2 : \mathbf{v}_2, \mathbf{S}_2 \\ \dots \\ \text{so, } \mathbf{E}, \mathbf{S}_{n-1} \vdash \mathbf{e}_n : \mathbf{v}_n, \mathbf{S}_n \end{array}}{\text{so, } \mathbf{E}, \mathbf{S} \vdash \{ \mathbf{e}_1 ; \dots ; \mathbf{e}_n ; \} : \mathbf{v}_n, \mathbf{S}_n}$$

- Again, the threading of the store expresses the intended execution sequence
- Only the **last value** is used
- But, **all side-effects** are collected (how?)

Operational Semantics for While (1)

Operational Semantics for While (1)

$$\frac{\text{so, } \mathbf{E}, \mathbf{S} \vdash e_1 : \text{Bool(false)}, \mathbf{S}_1}{\text{so, } \mathbf{E}, \mathbf{S} \vdash \text{while } e_1 \text{ loop } e_2 \text{ pool} : \text{void}, \mathbf{S}_1}$$

Operational Semantics for While (1)

$$\frac{\text{so, } \mathbf{E}, \mathbf{S} \vdash e_1 : \mathbf{Bool(false)}, \mathbf{S}_1}{\text{so, } \mathbf{E}, \mathbf{S} \vdash \text{while } e_1 \text{ loop } e_2 \text{ pool} : \mathbf{void}, \mathbf{S}_1}$$

- If e_1 evaluates to $\mathbf{Bool(false)}$, then the loop terminates immediately

Operational Semantics for While (1)

$$\frac{\text{so, } \mathbf{E}, \mathbf{S} \vdash \mathbf{e}_1 : \mathbf{Bool(false)}, \mathbf{S}_1}{\text{so, } \mathbf{E}, \mathbf{S} \vdash \mathbf{while\ e}_1 \mathbf{loop\ e}_2 \mathbf{pool : void}, \mathbf{S}_1}$$

- If \mathbf{e}_1 evaluates to $\mathbf{Bool(false)}$, then the loop terminates immediately
 - with the side-effects from the evaluation of \mathbf{e}_1

Operational Semantics for While (1)

$$\frac{\text{so, } \mathbf{E}, \mathbf{S} \vdash e_1 : \text{Bool(false)}, \mathbf{S}_1}{\text{so, } \mathbf{E}, \mathbf{S} \vdash \text{while } e_1 \text{ loop } e_2 \text{ pool} : \text{void}, \mathbf{S}_1}$$

- If e_1 evaluates to **Bool(false)**, then the loop terminates immediately
 - with the side-effects from the evaluation of e_1
 - and with the (arbitrary) result of **void**

Operational Semantics for While (1)

$$\frac{\text{so, } \mathbf{E}, \mathbf{S} \vdash e_1 : \mathbf{Bool(false)}, \mathbf{S}_1}{\text{so, } \mathbf{E}, \mathbf{S} \vdash \text{while } e_1 \text{ loop } e_2 \text{ pool} : \mathbf{void}, \mathbf{S}_1}$$

- If e_1 evaluates to $\mathbf{Bool(false)}$, then the loop terminates immediately
 - with the side-effects from the evaluation of e_1
 - and with the (arbitrary) result of \mathbf{void}
- The type rules ensure that e_1 evaluates to a boolean object

Operational Semantics for While (1)

$$\frac{\text{so, } \mathbf{E}, \mathbf{S} \vdash e_1 : \mathbf{Bool}(\mathbf{false}), \mathbf{S}_1}{\text{so, } \mathbf{E}, \mathbf{S} \vdash \mathbf{while } e_1 \mathbf{loop } e_2 \mathbf{pool} : \mathbf{void}, \mathbf{S}_1}$$

- If e_1 evaluates to $\mathbf{Bool}(\mathbf{false})$, then the loop terminates immediately
 - with the signature $\mathbf{void}, \mathbf{S}_1$
 - and with the state \mathbf{S}_1
 - The type rules for the **while** loop are:
- In-class exercise: given this rule for a **false** loop guard, what do you think the rule for a **true** loop guard looks like?

 - In groups of 2 or 3, write down a rule.
 - I will collect these; put your UCIDs/emails on it (mostly graded on completion)

Operational Semantics for While (2)

$$\frac{\begin{array}{l} \text{so, } E, S \vdash e_1 : \text{Bool}(\text{true}), S_1 \\ \text{so, } E, S_1 \vdash e_2 : v, S_2 \\ \text{so, } E, S_2 \vdash \text{while } e_1 \text{ loop } e_2 \text{ pool} : \text{void}, S_3 \end{array}}{\text{so, } E, S \vdash \text{while } e_1 \text{ loop } e_2 \text{ pool} : \text{void}, S_3}$$

Operational Semantics for While (2)

$$\frac{\begin{array}{l} \text{so, } \mathbf{E}, \mathbf{S} \vdash e_1 : \text{Bool(true)}, \mathbf{S}_1 \\ \text{so, } \mathbf{E}, \mathbf{S}_1 \vdash e_2 : v, \mathbf{S}_2 \\ \text{so, } \mathbf{E}, \mathbf{S}_2 \vdash \text{while } e_1 \text{ loop } e_2 \text{ pool} : \text{void}, \mathbf{S}_3 \end{array}}{\text{so, } \mathbf{E}, \mathbf{S} \vdash \text{while } e_1 \text{ loop } e_2 \text{ pool} : \text{void}, \mathbf{S}_3}$$

- Note the sequencing ($\mathbf{S} \rightarrow \mathbf{S}_1 \rightarrow \mathbf{S}_2 \rightarrow \mathbf{S}_3$)

Operational Semantics for While (2)

$$\frac{\begin{array}{l} \text{so, } \mathbf{E}, \mathbf{S} \vdash \mathbf{e}_1 : \mathbf{Bool}(\mathbf{true}), \mathbf{S}_1 \\ \text{so, } \mathbf{E}, \mathbf{S}_1 \vdash \mathbf{e}_2 : \mathbf{v}, \mathbf{S}_2 \\ \text{so, } \mathbf{E}, \mathbf{S}_2 \vdash \mathbf{while } \mathbf{e}_1 \mathbf{ loop } \mathbf{e}_2 \mathbf{ pool} : \mathbf{void}, \mathbf{S}_3 \end{array}}{\text{so, } \mathbf{E}, \mathbf{S} \vdash \mathbf{while } \mathbf{e}_1 \mathbf{ loop } \mathbf{e}_2 \mathbf{ pool} : \mathbf{void}, \mathbf{S}_3}$$

- Note the sequencing ($\mathbf{S} \rightarrow \mathbf{S}_1 \rightarrow \mathbf{S}_2 \rightarrow \mathbf{S}_3$)
- Note how looping is expressed (**recursively!**)
 - Evaluation of a **while** loop is expressed in terms of evaluating a **while** loop in another state

Operational Semantics for While (2)

$$\frac{\begin{array}{l} \text{so, } \mathbf{E}, \mathbf{S} \vdash \mathbf{e}_1 : \mathbf{Bool}(\mathbf{true}), \mathbf{S}_1 \\ \text{so, } \mathbf{E}, \mathbf{S}_1 \vdash \mathbf{e}_2 : \mathbf{v}, \mathbf{S}_2 \\ \text{so, } \mathbf{E}, \mathbf{S}_2 \vdash \mathbf{while} \ \mathbf{e}_1 \ \mathbf{loop} \ \mathbf{e}_2 \ \mathbf{pool} : \mathbf{void}, \mathbf{S}_3 \end{array}}{\text{so, } \mathbf{E}, \mathbf{S} \vdash \mathbf{while} \ \mathbf{e}_1 \ \mathbf{loop} \ \mathbf{e}_2 \ \mathbf{pool} : \mathbf{void}, \mathbf{S}_3}$$

- Note the sequencing ($\mathbf{S} \rightarrow \mathbf{S}_1 \rightarrow \mathbf{S}_2 \rightarrow \mathbf{S}_3$)
- Note how looping is expressed (**recursively!**)
 - Evaluation of a **while** loop is expressed in terms of evaluating a **while** loop in another state
- The result of evaluating \mathbf{e}_2 is discarded; only the side-effects are kept

Operational Semantics for Let (1)

Operational Semantics for Let (1)

$\text{so}, E, S \vdash e_1 : v_1, S_1$

$\text{so}, ?, ? \vdash e_2 : v_2, S_2$

$\text{so}, E, S_1 \vdash \text{let id} : T \leftarrow e_1 \text{ in } e_2 : v_2, S_2$

Operational Semantics for Let (1)

$$\text{so, } \mathbf{E}, \mathbf{S} \vdash \mathbf{e}_1 : \mathbf{v}_1, \mathbf{S}_1$$
$$\text{so, } \mathbf{?, ?} \vdash \mathbf{e}_2 : \mathbf{v}_2, \mathbf{S}_2$$

$$\text{so, } \mathbf{E}, \mathbf{S}_1 \vdash \text{let id : T} \leftarrow \mathbf{e}_1 \text{ in } \mathbf{e}_2 : \mathbf{v}_2, \mathbf{S}_2$$

- What is the context in which \mathbf{e}_2 should be evaluated?

Operational Semantics for Let (1)

$$\text{so, } \mathbf{E}, \mathbf{S} \vdash \mathbf{e}_1 : \mathbf{v}_1, \mathbf{S}_1$$
$$\text{so, } \mathbf{?, ?} \vdash \mathbf{e}_2 : \mathbf{v}_2, \mathbf{S}_2$$

$$\text{so, } \mathbf{E}, \mathbf{S}_1 \vdash \text{let } \mathbf{id} : \mathbf{T} <- \mathbf{e}_1 \text{ in } \mathbf{e}_2 : \mathbf{v}_2, \mathbf{S}_2$$

- What is the context in which \mathbf{e}_2 should be evaluated?
 - Environment should be like \mathbf{E} but with a new binding of \mathbf{id} to a fresh location \mathbf{l}_{new}

Operational Semantics for Let (1)

$$\begin{array}{l} \text{so, } \mathbf{E}, \mathbf{S} \vdash \mathbf{e}_1 : \mathbf{v}_1, \mathbf{S}_1 \\ \text{so, } \mathbf{?, ?} \vdash \mathbf{e}_2 : \mathbf{v}_2, \mathbf{S}_2 \end{array}$$

$$\text{so, } \mathbf{E}, \mathbf{S}_1 \vdash \text{let } \mathbf{id} : \mathbf{T} \leftarrow \mathbf{e}_1 \text{ in } \mathbf{e}_2 : \mathbf{v}_2, \mathbf{S}_2$$

- What is the context in which \mathbf{e}_2 should be evaluated?
 - Environment should be like \mathbf{E} but with a new binding of \mathbf{id} to a fresh location \mathbf{l}_{new}
 - Store like \mathbf{S}_1 but with \mathbf{l}_{new} mapped to \mathbf{v}_1

Operational Semantics for Let (2)

- We write $l_{\text{new}} = \text{newloc}(S)$ to say that l_{new} is a location that is not already used in S

Operational Semantics for Let (2)

- We write $l_{\text{new}} = \text{newloc}(S)$ to say that l_{new} is a location that is not already used in S
 - Think of newloc as the dynamic memory allocation function (or as reserving stack space)

Operational Semantics for Let (2)

- We write $l_{\text{new}} = \text{newloc}(S)$ to say that l_{new} is a location that is not already used in S
 - Think of newloc as the dynamic memory allocation function (or as reserving stack space)
- This lets^(haha) us write the correct let rule:

$$\frac{\begin{array}{c} \text{so, } E, S \vdash e_1 : v_1, S_1 \\ l_{\text{new}} = \text{newloc}(S_1) \\ \text{so, } E[l_{\text{new}}/\text{id}], S_1[v_1/l_{\text{new}}] \vdash e_2 : v_2, S_2 \end{array}}{\text{so, } E, S_1 \vdash \text{let id : T} \leftarrow e_1 \text{ in } e_2 : v_2, S_2}$$

Warning: The Going Gets Tough

- Now we're going to do some **very difficult** rules

Warning: The Going Gets Tough

- Now we're going to do some **very difficult** rules
 - **new**, dispatch

Warning: The Going Gets Tough

- Now we're going to do some **very difficult** rules
 - **new**, dispatch
- This may initially seem tricky
 - How could that possibly work?
 - What's going on here?

Warning: The Going Gets Tough

- Now we're going to do some **very difficult** rules
 - **new**, dispatch
- This may initially seem tricky
 - How could that possibly work?
 - What's going on here?
- Once you've studied them a bit, hopefully you'll agree they're actually **quite elegant**
 - But they will probably not seem that way at first

Operational Semantics of **new**

- Consider the expression **new** T
- Its informal semantics are:

Operational Semantics of **new**

- Consider the expression **new** T
- Its informal semantics are:
 - Allocate new locations to hold the values for all attributes of an object of class T
 - Essentially, allocate space for a new object

Operational Semantics of **new**

- Consider the expression **new** T
- Its informal semantics are:
 - Allocate new locations to hold the values for all attributes of an object of class T
 - Essentially, allocate space for a new object
 - Initialize those locations with the **default values** of attributes

Operational Semantics of **new**

- Consider the expression **new** T
- Its informal semantics are:
 - Allocate new locations to hold the values for all attributes of an object of class T
 - Essentially, allocate space for a new object
 - Initialize those locations with the **default values** of attributes
 - Evaluate the initializers and set the resulting attribute values

Operational Semantics of **new**

- Consider the expression **new** T
- Its informal semantics are:
 - Allocate new locations to hold the values for all attributes of an object of class T
 - Essentially, allocate space for a new object
 - Initialize those locations with the **default values** of attributes
 - Evaluate the initializers and set the resulting attribute values
 - Return the newly allocated object

Default Values

- For each class A there is a default value denoted by D_A

Default Values

- For each class **A** there is a default value denoted by D_A
 - $D_{Int} = Int(0)$

Default Values

- For each class **A** there is a default value denoted by D_A
 - $D_{Int} = Int(0)$
 - $D_{Bool} = Bool(0)$

Default Values

- For each class **A** there is a default value denoted by D_A
 - $D_{Int} = Int(0)$
 - $D_{Bool} = Bool(0)$
 - $D_{String} = String(0, "")$

Default Values

- For each class **A** there is a default value denoted by D_A
 - $D_{Int} = Int(0)$
 - $D_{Bool} = Bool(0)$
 - $D_{String} = String(0, "")$
 - $D_A = void$ for all other classes **A**

More Notation

More Notation

- For each class **A** we write

$$\text{class}(\mathbf{A}) = (\mathbf{a}_1 : \mathbf{T}_1 \leftarrow \mathbf{e}_1, \dots, \mathbf{a}_n : \mathbf{T}_n \leftarrow \mathbf{e}_n)$$

where

More Notation

- For each class **A** we write

$$\text{class}(\mathbf{A}) = (\mathbf{a}_1 : \mathbf{T}_1 \leftarrow \mathbf{e}_1, \dots, \mathbf{a}_n : \mathbf{T}_n \leftarrow \mathbf{e}_n)$$

where

- \mathbf{a}_i are the attributes (including inherited ones)

More Notation

- For each class **A** we write

$$\text{class}(\mathbf{A}) = (\mathbf{a}_1 : \mathbf{T}_1 \leftarrow \mathbf{e}_1, \dots, \mathbf{a}_n : \mathbf{T}_n \leftarrow \mathbf{e}_n)$$

where

- \mathbf{a}_i are the attributes (including inherited ones)
- \mathbf{T}_i are their declared types

More Notation

- For each class **A** we write

$$\text{class}(\mathbf{A}) = (\mathbf{a}_1 : \mathbf{T}_1 \leftarrow \mathbf{e}_1, \dots, \mathbf{a}_n : \mathbf{T}_n \leftarrow \mathbf{e}_n)$$

where

- \mathbf{a}_i are the attributes (including inherited ones)
- \mathbf{T}_i are their declared types
- \mathbf{e}_i are the initializers (including default values)

More Notation

This is exactly the
class map from PA2!

- For each class **A** we write

$$\text{class}(\mathbf{A}) = (\mathbf{a}_1 : \mathbf{T}_1 \leftarrow \mathbf{e}_1, \dots, \mathbf{a}_n : \mathbf{T}_n \leftarrow \mathbf{e}_n)$$

where

- \mathbf{a}_i are the attributes (including inherited ones)
- \mathbf{T}_i are their declared types
- \mathbf{e}_i are the initializers (including default values)

Operational Semantics for new



Operational Semantics for **new**

T_0 = if T = **SELF_TYPE** and $so = X(...)$ then X else T

$\text{class}(T_0) = (a_1 : T_1 \leftarrow e_1, \dots, a_n : T_n \leftarrow e_n)$

$\forall i \in [1 \dots n], l_i = \text{newloc}(S)$

$v = T_0(a_1 = l_1, \dots, a_n = l_n)$

$S_1 = S[D_{T_1}/l_1, \dots, D_{T_n}/l_n]$

$E' = [a_1 : l_1, \dots, a_n : l_n]$

$so, E', S_1 \vdash \{a_1 \leftarrow e_1 ; \dots ; a_n \leftarrow e_n ; \} : v_n, S_2$

$so, E, S \vdash \text{new } T : v, S_2$

Operational Semantics for **new**

$T_0 = \text{if } T = \text{SELF_TYPE} \text{ and } \text{so} = X(...) \text{ then } X \text{ else } T$

$\text{class}(T_0) = (a_1 : T_1 \leftarrow e_1, \dots, a_n : T_n \leftarrow e_n)$

$\forall i \in [1 \dots n], l_i = \text{newloc}(S)$

$v = T_0(a_1 = l_1, \dots, a_n = l_n)$

$S_1 = S[D_{T_1}/l_1, \dots, D_{T_n}/l_n]$

$E' = [a_1 : l_1, \dots, a_n : l_n]$

$\text{so}, E', S_1 \vdash \{a_1 \leftarrow e_1 ; \dots ; a_n \leftarrow e_n ;\} : v_n, S_2$

$\text{so}, E, S \vdash \text{new } T : v, S_2$

if the desired type is SELF_TYPE, use the so object; otherwise use the type named in the expression (T)

Operational Semantics for **new**

$T_0 = \text{if } T = \text{SELF_TYPE} \text{ and } \text{so} = X(\dots) \text{ then } X \text{ else } T$

$\text{class}(T_0) = (a_1 : T_1 \leftarrow e_1, \dots, a_n : T_n \leftarrow e_n)$

$\forall i \in [1 \dots n], l_i = \text{newloc}(S)$

$v = T_0(a_1 = l_1, \dots, a_n = l_n)$

$S_1 = S[D_{T_1}/l_1, \dots, D_{T_n}/l_n]$

$E' = [a_1 : l_1, \dots, a_n : l_n]$

$\text{so}, E', S_1 \vdash \{a_1 \leftarrow e_1 ; \dots ; a_n \leftarrow e_n ; \} : v_n, S_2$

$\text{so}, E, S \vdash \text{new } T : v, S_2$

fetch the template
for the class to
instantiate

Operational Semantics for **new**

T_0 = if $T = \text{SELF_TYPE}$ and $\text{so} = X(\dots)$ then X else T

$\text{class}(T_0) = (a_1 : T_1 \leftarrow e_1, \dots, a_n : T_n \leftarrow e_n)$

$\forall i \in [1 \dots n], l_i = \text{newloc}(S)$

$v = T_0(a_1 = l_1, \dots, a_n = l_n)$

$S_1 = S[D_{T_1}/l_1, \dots, D_{T_n}/l_n]$

$E' = [a_1 : l_1, \dots, a_n : l_n]$

$\text{so}, E', S_1 \vdash \{a_1 \leftarrow e_1 ; \dots ; a_n \leftarrow e_n ; \} : v_n, S_2$

$\text{so}, E, S \vdash \text{new } T : v, S_2$

make space for each
of its attributes (now,
we've *allocated* the
new object)

Operational Semantics for **new**

$T_0 = \text{if } T = \text{SELF_TYPE} \text{ and } \text{so} = X(\dots) \text{ then } X \text{ else } T$

$\text{class}(T_0) = (a_1 : T_1 \leftarrow e_1, \dots, a_n : T_n \leftarrow e_n)$

$\forall i \in [1 \dots n], l_i = \text{newloc}(S)$

$v = T_0(a_1 = l_1, \dots, a_n = l_n)$

$S_1 = S[D_{T_1}/l_1, \dots, D_{T_n}/l_n]$

$E' = [a_1 : l_1, \dots, a_n : l_n]$

$\text{so}, E', S_1 \vdash \{a_1 \leftarrow e_1 ; \dots ; a_n \leftarrow e_n ; \} : v_n, S_2$

$\text{so}, E, S \vdash \text{new } T : v, S_2$

create a new value for the newly-created object; make each attribute point to the appropriate new location (this step is the start of *initialization*)

Operational Semantics for **new**

$T_0 = \text{if } T = \text{SELF_TYPE} \text{ and } \text{so} = X(\dots) \text{ then } X \text{ else } T$

$\text{class}(T_0) = (a_1 : T_1 \leftarrow e_1, \dots, a_n : T_n \leftarrow e_n)$

$\forall i \in [1 \dots n], l_i = \text{newloc}(S)$

$v = T_0(a_1 = l_1, \dots, a_n = l_n)$

$S_1 = S[D_{T_1}/l_1, \dots, D_{T_n}/l_n]$

$E' = [a_1 : l_1, \dots, a_n : l_n]$

$\text{so}, E', S_1 \vdash \{a_1 \leftarrow e_1 ; \dots ; a_n \leftarrow e_n ; \} : v_n, S_2$

$\text{so}, E, S \vdash \text{new } T : v, S_2$

create a new store with each new attribute location set to the default value for its type



Operational Semantics for **new**

T_0 = if T = **SELF_TYPE** and $so = X(...)$ then X else T

$\text{class}(T_0) = (a_1 : T_1 \leftarrow e_1, \dots, a_n : T_n \leftarrow e_n)$

$\forall i \in [1 \dots n], l_i = \text{newloc}(S)$

$v = T_0(a_1 = l_1, \dots, a_n = l_n)$

$S_1 = S[D_{T_1}/l_1, \dots, D_{T_n}/l_n]$

$E' = [a_1 : l_1, \dots, a_n : l_n]$

create a new environment
with only the attributes
in-scope (in which to
evaluate the initializers)

$so, E', S_1 \vdash \{a_1 \leftarrow e_1 ; \dots ; a_n \leftarrow e_n ; \} : v_n, S_2$

$so, E, S \vdash \text{new } T : v, S_2$

Operational Semantics for **new**

$T_0 = \text{if } T = \text{SELF_TYPE} \text{ and } \text{so} = X(\dots) \text{ then } X \text{ else } T$

$\text{class}(T_0) = (a_1 : T_1 \leftarrow e_1, \dots, a_n : T_n \leftarrow e_n)$

$\forall i \in [1 \dots n], l_i = \text{newloc}(S)$

$v = T_0(a_1 = l_1, \dots, a_n = l_n)$

$S_1 = S[D_{T_1}/l_1, \dots, D_{T_n}/l_n]$

$E' = [a_1 : l_1, \dots, a_n : l_n]$

$\text{so}, E', S_1 \vdash \{a_1 \leftarrow e_1 ; \dots ; a_n \leftarrow e_n ; \} : v_n, S_2$

evaluate all of the initializers,
keeping the side-effects in S_2

$\text{so}, E, S \vdash \text{new } T : v, S_2$

Trivia Break: Real World Languages

English is the single-most widely-spoken and only official language in this West African country, which, with over 230 million people, is the most populous country in Africa (and its former capital, Lagos, is one of Africa's largest cities). The country's linguistic diversity is a microcosm of Africa as a whole, with significant numbers of native speakers of languages from the three major African language families: Afroasiatic, Nilo-Saharan and Niger-Congo.

Name the country and any one language of African origin that is spoken there by at least 2 million people.

Trivia Break: Math

This Austrian mathematician moved to New Jersey in a rather circuitous way: after the Anschluss in 1938, the Nazis found him - previously a lecturer at the University of Vienna - fit for conscription. He fled across the Soviet Union, sailed to Japan and then on to San Francisco, and then traveled across the US to take up a position at the Institute for Advanced Study (IAS) in Princeton. Toward the end of his own life, fellow IAS researcher Albert Einstein confided that his "own work no longer meant much, that he came to the Institute merely ... to have the privilege of walking home with [him]". Though his work spanned several areas of mathematics, philosophy, and logic, he is most famous for his Incompleteness Theorem.

Operational Semantics of Method Dispatch

- Consider the expression $e_0.f(e_1, \dots, e_n)$
- Its informal semantics are:

Operational Semantics of Method Dispatch

- Consider the expression $e_\theta . f(e_1, \dots, e_n)$
- Its informal semantics are:
 - Evaluate the arguments in order e_1, \dots, e_n

Operational Semantics of Method Dispatch

- Consider the expression $e_0.f(e_1, \dots, e_n)$
- Its informal semantics are:
 - Evaluate the arguments in order e_1, \dots, e_n
 - Evaluate e_0 to the target object

Operational Semantics of Method Dispatch

- Consider the expression $e_0.f(e_1, \dots, e_n)$
- Its informal semantics are:
 - Evaluate the arguments in order e_1, \dots, e_n
 - Evaluate e_0 to the target object
 - Let X be the dynamic type of the target object

Operational Semantics of Method Dispatch

- Consider the expression $e_0.f(e_1, \dots, e_n)$
- Its informal semantics are:
 - Evaluate the arguments in order e_1, \dots, e_n
 - Evaluate e_0 to the target object
 - Let X be the dynamic type of the target object
 - Fetch from X the definition of f (with n args)

Operational Semantics of Method Dispatch

- Consider the expression $e_0.f(e_1, \dots, e_n)$
- Its informal semantics are:
 - Evaluate the arguments in order e_1, \dots, e_n
 - Evaluate e_0 to the target object
 - Let X be the dynamic type of the target object
 - Fetch from X the definition of f (with n args)
 - Create n new locations and an environment that maps f 's formal arguments to those locations

Operational Semantics of Method Dispatch

- Consider the expression $e_0.f(e_1, \dots, e_n)$
- Its informal semantics are:
 - Evaluate the arguments in order e_1, \dots, e_n
 - Evaluate e_0 to the target object
 - Let X be the dynamic type of the target object
 - Fetch from X the definition of f (with n args)
 - Create n new locations and an environment that maps f 's formal arguments to those locations
 - Initialize the locations with the actual arguments

Operational Semantics of Method Dispatch

- Consider the expression $e_0.f(e_1, \dots, e_n)$
- Its informal semantics are:
 - Evaluate the arguments in order e_1, \dots, e_n
 - Evaluate e_0 to the target object
 - Let X be the dynamic type of the target object
 - Fetch from X the definition of f (with n args)
 - Create n new locations and an environment that maps f 's formal arguments to those locations
 - Initialize the locations with the actual arguments
 - Set self to the target object and evaluate f 's body

More Notation

- For a class **A** and a method **f** of **A** (possibly inherited) we write:

More Notation

- For a class **A** and a method **f** of **A** (possibly inherited) we write:

$$\text{imp}(\mathbf{A}, \mathbf{f}) = (\mathbf{x}_1, \dots, \mathbf{x}_n, \mathbf{e}_{\text{body}})$$

where:

More Notation

- For a class **A** and a method **f** of **A** (possibly inherited) we write:

$$\text{imp}(\mathbf{A}, \mathbf{f}) = (\mathbf{x}_1, \dots, \mathbf{x}_n, \mathbf{e}_{\text{body}})$$

where:

- \mathbf{x}_i are the names of the formal arguments

More Notation

- For a class **A** and a method **f** of **A** (possibly inherited) we write:

$$\text{imp}(\mathbf{A}, \mathbf{f}) = (\mathbf{x}_1, \dots, \mathbf{x}_n, \mathbf{e}_{\text{body}})$$

where:

- \mathbf{x}_i are the names of the formal arguments
- \mathbf{e}_{body} is the body of the method

More Notation

- For a class **A** and a method **f** of **A** (possibly inherited) we write:

$$\text{imp}(\mathbf{A}, \mathbf{f}) = (\mathbf{x}_1, \dots, \mathbf{x}_n, \mathbf{e}_{\text{body}})$$

where:

- \mathbf{x}_i are the names of the formal arguments
- \mathbf{e}_{body} is the body of the method

This is exactly the
implementation map
from PA2!

Operational Semantics for Method Dispatch



Once more
unto the breach,
dear friends,
once more!

Operational Semantics for Method Dispatch

so, $E, S \vdash e_1 : v_1, S_1$

so, $E, S_1 \vdash e_2 : v_2, S_2$

...

so, $E, S_{n-1} \vdash e_n : v_n, S_n$

so, $E, S_n \vdash e_0 : v_0, S_{n+1}$

$v_0 = X(a_1 = l_1, \dots, a_m = l_m)$

$\text{imp}(X, f) = (x_1, \dots, x_n, e_{\text{body}})$

$\forall i \in [1 \dots n], l_{xi} = \text{newloc}(S_{n+1})$

$E' = [x_1 : l_{x1}, \dots, x_n : l_{xn}, a_1 : l_1, \dots, a_m : l_m]$

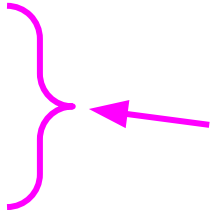
$S_{n+2} = S_{n+1}[v_1/l_{x1}, \dots, v_n/l_{xn}]$

$v_0, E', S_{n+2} \vdash e_{\text{body}} : v, S_{n+3}$

so, $E, S \vdash e_0.f(e_1, \dots, e_n) : v, S_{n+3}$

Operational Semantics for Method Dispatch

$\text{so, } E, S \vdash e_1 : v_1, S_1$
 \dots
 $\text{so, } E, S_{n-1} \vdash e_n : v_n, S_n$


 evaluate all of the arguments

$\text{so, } E, S_n \vdash e_0 : v_0, S_{n+1}$

$v_0 = X(a_1 = l_1, \dots, a_m = l_m)$

$\text{imp}(X, f) = (x_1, \dots, x_n, e_{\text{body}})$

$\forall i \in [1 \dots n], l_{xi} = \text{newloc}(S_{n+1})$

$E' = [x_1 : l_{x1}, \dots, x_n : l_{xn}, a_1 : l_1, \dots, a_m : l_m]$

$S_{n+2} = S_{n+1}[v_1/l_{x1}, \dots, v_n/l_{xn}]$

$v_0, E', S_{n+2} \vdash e_{\text{body}} : v, S_{n+3}$

$\text{so, } E, S \vdash e_0.f(e_1, \dots, e_n) : v, S_{n+3}$

Operational Semantics for Method Dispatch

so, $E, S \vdash e_1 : v_1, S_1$

so, $E, S_1 \vdash e_2 : v_2, S_2$

...

so, $E, S_{n-1} \vdash e_n : v_n, S_n$

so, $E, S_n \vdash e_0 : v_0, S_{n+1}$

$v_0 = X(a_1 = l_1, \dots, a_m = l_m)$

$\text{imp}(X, f) = (x_1, \dots, x_n, e_{\text{body}})$


$\forall i \in [1 \dots n], l_{xi} = \text{newloc}(S_{n+1})$

$E' = [x_1 : l_{x1}, \dots, x_n : l_{xn}, a_1 : l_1, \dots, a_m : l_m]$

$S_{n+2} = S_{n+1}[v_1/l_{x1}, \dots, v_n/l_{xn}]$

$v_0, E', S_{n+2} \vdash e_{\text{body}} : v, S_{n+3}$

evaluate the receiver
object (= object on
which method is called)



so, $E, S \vdash e_0.f(e_1, \dots, e_n) : v, S_{n+3}$

Operational Semantics for Method Dispatch

so, $E, S \vdash e_1 : v_1, S_1$

so, $E, S_1 \vdash e_2 : v_2, S_2$

...

so, $E, S_{n-1} \vdash e_n : v_n, S_n$

so, $E, S_n \vdash e_0 : v_0, S_{n+1}$

$v_0 = X(a_1 = l_1, \dots, a_m = l_m)$



find the receiver's type
and attributes

$\text{imp}(X, f) = (x_1, \dots, x_n, e_{\text{body}})$

$\forall i \in [1..n], l_{xi} = \text{newloc}(S_{n+1})$

$E' = [x_1 : l_{x1}, \dots, x_n : l_{xn}, a_1 : l_1, \dots, a_m : l_m]$

$S_{n+2} = S_{n+1}[v_1/l_{x1}, \dots, v_n/l_{xn}]$

$v_0, E', S_{n+2} \vdash e_{\text{body}} : v, S_{n+3}$

so, $E, S \vdash e_0.f(e_1, \dots, e_n) : v, S_{n+3}$

Operational Semantics for Method Dispatch

so, $E, S \vdash e_1 : v_1, S_1$

so, $E, S_1 \vdash e_2 : v_2, S_2$

...

so, $E, S_{n-1} \vdash e_n : v_n, S_n$

so, $E, S_n \vdash e_0 : v_0, S_{n+1}$

$v_0 = X(a_1 = l_1, \dots, a_m = l_m)$

$\text{imp}(X, f) = (x_1, \dots, x_n, e_{\text{body}})$  find formals and body

$\forall i \in [1..n], l_{xi} = \text{newloc}(S_{n+1})$

$E' = [x_1 : l_{x1}, \dots, x_n : l_{xn}, a_1 : l_1, \dots, a_m : l_m]$

$S_{n+2} = S_{n+1}[v_1/l_{x1}, \dots, v_n/l_{xn}]$

$v_0, E', S_{n+2} \vdash e_{\text{body}} : v, S_{n+3}$

so, $E, S \vdash e_0.f(e_1, \dots, e_n) : v, S_{n+3}$

Operational Semantics for Method Dispatch

so, $E, S \vdash e_1 : v_1, S_1$

so, $E, S_1 \vdash e_2 : v_2, S_2$

...

so, $E, S_{n-1} \vdash e_n : v_n, S_n$

so, $E, S_n \vdash e_0 : v_0, S_{n+1}$

$v_0 = X(a_1 = l_1, \dots, a_m = l_m)$

$\text{imp}(X, f) = (x_1, \dots, x_n, e_{\text{body}})$

$\forall i \in [1 \dots n], l_{x_i} = \text{newloc}(S_{n+1})$

call by reference or by value?

$E' = [x_1 : l_{x1}, \dots, x_n : l_{xn}, a_1 : l_1, \dots, a_m : l_m]$

$S_{n+2} = S_{n+1}[v_1/l_{x1}, \dots, v_n/l_{xn}]$

$v_0, E', S_{n+2} \vdash e_{\text{body}} : v, S_{n+3}$

so, $E, S \vdash e_0.f(e_1, \dots, e_n) : v, S_{n+3}$

Aside: Call by Reference vs Call by Value

Aside: Call by Reference vs Call by Value

- Cool uses *call by reference*
 - when a function is called, only the pointer to each argument is copied
 - modifications to the arguments are reflected at the call-site

Aside: Call by Reference vs Call by Value

- Cool uses *call by reference*
 - when a function is called, only the pointer to each argument is copied
 - modifications to the arguments are reflected at the call-site
- The alternative is *call by value*
 - when a function is called, a **full copy** of each argument is passed to the callee
 - this is fine for e.g., integers, but for objects it gets expensive quickly

Aside: Call by Reference vs Call by Value

- Cool uses *call by reference*
 - when a function is called, only the pointer to each argument is copied
 - modifications to the arguments are reflected at the call-site
- The alternative is *call by value*
 - when a function is called, a **full copy** of each argument is passed to the callee
 - this is fine for e.g., integers, but for objects it gets expensive quickly
- Which does C support?

Operational Semantics for Method Dispatch

so, $E, S \vdash e_1 : v_1, S_1$

so, $E, S_1 \vdash e_2 : v_2, S_2$

...

so, $E, S_{n-1} \vdash e_n : v_n, S_n$

so, $E, S_n \vdash e_0 : v_0, S_{n+1}$

$v_0 = X(a_1 = l_1, \dots, a_m = l_m)$

$\text{imp}(X, f) = (x_1, \dots, x_n, e_{\text{body}})$

$\forall i \in [1 \dots n], l_{x_i} = \text{newloc}(S_{n+1})$

$E' = [x_1 : l_{x_1}, \dots, x_n : l_{x_n}, a_1 : l_1, \dots, a_m : l_m]$

$S_{n+2} = S_{n+1}[v_1/l_{x_1}, \dots, v_n/l_{x_n}]$

$v_0, E', S_{n+2} \vdash e_{\text{body}} : v, S_{n+3}$

call by reference, only
allocate space for
copies of the pointers

so, $E, S \vdash e_0.f(e_1, \dots, e_n) : v, S_{n+3}$

Operational Semantics for Method Dispatch

so, $E, S \vdash e_1 : v_1, S_1$

so, $E, S_1 \vdash e_2 : v_2, S_2$

...

so, $E, S_{n-1} \vdash e_n : v_n, S_n$

so, $E, S_n \vdash e_0 : v_0, S_{n+1}$

$v_0 = X(a_1 = l_1, \dots, a_m = l_m)$

$\text{imp}(X, f) = (x_1, \dots, x_n, e_{\text{body}})$


$\forall i \in [1 \dots n], l_{x_i} = \text{newloc}(S_{n+1})$

$E' = [x_1 : l_{x1}, \dots, x_n : l_{xn}, a_1 : l_1, \dots, a_m : l_m]$

$S_{n+2} = S_{n+1}[v_1/l_{x1}, \dots, v_n/l_{xn}]$

$v_0, E', S_{n+2} \vdash e_{\text{body}} : v, S_{n+3}$

new environment, with
the formals and the
attributes of the
receiver in-scope



so, $E, S \vdash e_0.f(e_1, \dots, e_n) : v, S_{n+3}$

Operational Semantics for Method Dispatch

$\text{so}, E, S \vdash$
 \dots
 so, E, S_n
 $v_0 = X(a_1$
 $\text{imp}(X, f) = (x_1, \dots, x_n, e_{\text{body}})$
 $\forall i \in [1 \dots n], l_{x_i} = \text{newloc}(S_{n+1})$
 $E' = [x_1 : l_{x_1}, \dots, x_n : l_{x_n}, a_1 : l_1, \dots, a_m : l_m]$
 $S_{n+2} = S_{n+1}[v_1 / l_{x_1}, \dots, v_n / l_{x_n}]$
 $v_0, E', S_{n+2} \vdash e_{\text{body}} : v, S_{n+3}$

Do you think the **order matters** here? What could go wrong if the formals were after the attributes?

new environment, with the formals and the attributes of the receiver in-scope

$\text{so}, E, S \vdash e_0.f(e_1, \dots, e_n) : v, S_{n+3}$

Operational Semantics for Method Dispatch

so, $E, S \vdash e_1 : v_1, S_1$

so, $E, S_1 \vdash e_2 : v_2, S_2$

...

so, $E, S_{n-1} \vdash e_n : v_n, S_n$

so, $E, S_n \vdash e_0 : v_0, S_{n+1}$

$v_0 = X(a_1 = l_1, \dots, a_m = l_m)$

$\text{imp}(X, f) = (x_1, \dots, x_n, e_{\text{body}})$

$\forall i \in [1..n], l_{xi} = \text{newloc}(S_{n+1})$

$E' = [x_1 : l_{x1}, \dots, x_n : l_{xn}, a_1 : l_1, \dots, a_m : l_m]$

$S_{n+2} = S_{n+1}[v_1/l_{x1}, \dots, v_n/l_{xn}]$

$v_0, E', S_{n+2} \vdash e_{\text{body}} : v, S_{n+3}$

new store with formals
pointing to the actual
arguments

so, $E, S \vdash e_0.f(e_1, \dots, e_n) : v, S_{n+3}$

Operational Semantics for Method Dispatch

so, $E, S \vdash e_1 : v_1, S_1$

so, $E, S_1 \vdash e_2 : v_2, S_2$

...

so, $E, S_{n-1} \vdash e_n : v_n, S_n$

so, $E, S_n \vdash e_0 : v_0, S_{n+1}$

$v_0 = X(a_1 = l_1, \dots, a_m = l_m)$

$\text{imp}(X, f) = (x_1, \dots, x_n, e_{\text{body}})$

$\forall i \in [1 \dots n], l_{xi} = \text{newloc}(S_{n+1})$

$E' = [x_1 : l_{x1}, \dots, x_n : l_{xn}, a_1 : l_1, \dots, a_m : l_m]$

$S_{n+2} = S_{n+1}[v_1/l_{x1}, \dots, v_n/l_{xn}]$

$v_0, E', S_{n+2} \vdash e_{\text{body}} : v, S_{n+3}$

finally, evaluate the
body

so, $E, S \vdash e_0.f(e_1, \dots, e_n) : v, S_{n+3}$

Notes on OpSem for Dispatch

- The body of the method is invoked with:

Notes on OpSem for Dispatch

- The body of the method is invoked with:
 - **E'** mapping formal arguments and self's attributes

Notes on OpSem for Dispatch

- The body of the method is invoked with:
 - E' mapping formal arguments and self's attributes
 - S_{n+2} like the caller's except with actual arguments bound to the locations allocated for formals

Notes on OpSem for Dispatch

- The body of the method is invoked with:
 - **E'** mapping formal arguments and self's attributes
 - **S_{n+2}** like the caller's except with actual arguments bound to the locations allocated for formals
- The notion of the **activation frame** is implicit
 - New locations are allocated for actual arguments

Notes on OpSem for Dispatch

- The body of the method is invoked with:
 - **E'** mapping formal arguments and self's attributes
 - **S_{n+2}** like the caller's except with actual arguments bound to the locations allocated for formals
- The notion of the **activation frame** is implicit
 - New locations are allocated for actual arguments
- The semantics of **static dispatch** is similar except the implementation of **f** is taken from the specified class

Run-time Errors

Run-time Errors

- The operational semantics **do not** cover all possible cases!

Run-time Errors

- The operational semantics **do not** cover all possible cases!
- Consider for example this bit from the dispatch rule:

...

so, $E, S_n \vdash e_0 : v_0, S_{n+1}$

$v_0 = X(a_1 = l_1, \dots, a_m = l_m)$

$\text{imp}(X, f) = (x_1, \dots, x_n, e_{\text{body}})$

...

- What happens if $\text{imp}(X, f)$ is not defined?

Run-time Errors

- The operational semantics **do not** cover all possible cases!
- Consider for example this bit from the dispatch rule:

...

so, $E, S_n \vdash e_0 : v_0, S_{n+1}$

$v_0 = X(a_1 = l_1, \dots, a_m = l_m)$

$\text{imp}(X, f) = (x_1, \dots, x_n, e_{\text{body}})$

...

- What happens if $\text{imp}(X, f)$ is not defined?
 - It **cannot be**! Type safety theorem guarantees it :)

Run-time Errors

- There are some run-time errors that the typechecker **does not try** to prevent (but it could - we'll get to it in a few minutes)

Run-time Errors

- There are some run-time errors that the typechecker **does not try** to prevent (but it could - we'll get to it in a few minutes)
 - dispatching on void
 - division by zero
 - substring out of range
 - heap overflow

Run-time Errors

- There are some run-time errors that the typechecker **does not try** to prevent (but it could - we'll get to it in a few minutes)
 - dispatching on void
 - division by zero
 - substring out of range
 - heap overflow
- In such cases the execution must **abort gracefully**

Run-time Errors

- There are some run-time errors that the typechecker **does not try** to prevent (but it could - we'll get to it in a few minutes)
 - dispatching on void
 - division by zero
 - substring out of range
 - heap overflow
- In such cases the execution must **abort gracefully**
 - i.e., with an error message rather than a segfault

Run-time Errors

- There are some run-time errors that the typechecker **does not try** to prevent (but it could - we'll get to it in a few minutes)
 - dispatching on void
 - division by zero
 - substring out of range
 - heap overflow
- In such cases the execution must **abort gracefully**
 - i.e., with an error message rather than a segfault
 - implication: you must generate code in PA3 that **checks for run-time errors!**

Summary of Operational Semantics

Summary of Operational Semantics

- Operational rules are **very precise**
 - Nothing is left unspecified

Summary of Operational Semantics

- Operational rules are **very precise**
 - Nothing is left unspecified
- Operational rules contain a lot of details
 - Read them **carefully**!

Summary of Operational Semantics

- Operational rules are **very precise**
 - Nothing is left unspecified
- Operational rules contain a lot of details
 - Read them **carefully**!
- Most languages **do not** have a well-specified operational semantics :(

Summary of Operational Semantics

- Operational rules are **very precise**
 - Nothing is left unspecified
- Operational rules contain a lot of details
 - Read them **carefully**!
- Most languages **do not** have a well-specified operational semantics :(
- When **portability** is important, an operational semantics is essential
 - But typically not using the exact notation we used for Cool

Agenda

- Review: basics of operational semantics
- Operational semantics of Cool
- **(if time): introduction to static analysis**
 - further if time: get into abstract interpretation

Motivation: many defects are hard to test for

Motivation: many defects are hard to test for

- Many interesting defects are on **uncommon** or **difficult-to-exercise** execution paths

Motivation: many defects are hard to test for

- Many interesting defects are on **uncommon** or **difficult-to-exercise** execution paths
 - So it's hard to find them via testing

Motivation: many defects are hard to test for

- Many interesting defects are on **uncommon** or **difficult-to-exercise** execution paths
 - So it's hard to find them via testing
- Executing or dynamically analyzing all paths concretely to find such defects is **not feasible** (cf. exhaustive testing is infeasible)

Motivation: many defects are hard to test for

- Many interesting defects are on **uncommon** or **difficult-to-exercise** execution paths
 - So it's hard to find them via testing
- Executing or dynamically analyzing all paths concretely to find such defects is **not feasible** (cf. exhaustive testing is infeasible)
- We want to learn about “**all possible runs**” of the program for particular properties

Motivation: many defects are hard to test for

- Many interesting defects are on **uncommon** or **difficult-to-exercise** execution paths
 - So it's hard to find them via testing
- Executing or dynamically analyzing all paths concretely to find such defects is **not feasible** (cf. exhaustive testing is infeasible)
- We want to learn about “**all possible runs**” of the program for particular properties
 - Without actually running the program!

Motivation: many defects are hard to test for

- Many interesting defects are on **uncommon** or **difficult-to-exercise** execution paths
 - So it's hard to find them via testing
- Executing or dynamically analyzing all paths concretely to find such defects is **not feasible** (cf. exhaustive testing is infeasible)
- We want to learn about “**all possible runs**” of the program for particular properties
 - Without actually running the program!
 - Bonus: we don't need test cases!

Motivation: many defects are hard to test for

- Many interesting defects are on **uncommon** or **difficult-to-exercise** execution paths
 - So it's hard to find them via testing
- Executing or dynamically analyzing such defects is **not feasible** (cf. [1])
- We want to learn about “**all possible**” particular properties
 - Without actually running the program
 - Bonus: we don't need test cases

This is especially true for certain kinds of hard-to-test-for defects that might not be apparent even if you do exercise them, such as **resource leaks**

What does static analysis do well?

What does static analysis do well?

- Defects that result from inconsistently following **simple**, mechanical design **rules**

What does static analysis do well?

- Defects that result from inconsistently following **simple**, mechanical design **rules**
 - Security: buffer overruns, input validation
 - Memory safety: null pointers, initialized data
 - Resource leaks: memory, OS resources
 - API Protocols: device drivers, GUI frameworks
 - Exceptions: arithmetic, library, user-defined
 - Encapsulation: internal data, private functions
 - Data races: two threads, one variable

What does static analysis do well?

- Defects that result from inconsistent mechanical design **rules**

- Security: buffer overruns, input
- Memory safety: null pointers, in
- Resource leaks: memory, OS resources
- API Protocols: device drivers, GUI frameworks
- Exceptions: arithmetic, library, user-defined
- Encapsulation: internal data, private functions
- Data races: two threads, one variable

There are **rules** for doing each of these things **correctly**, and a static analysis can automate those rules.

What is a static analysis?

What is a static analysis?

Definition: *static analysis* is the systematic examination of an abstraction of program state space

What is a static analysis?

Definition: *static analysis* is the systematic examination of an abstraction of program state space

- static analysis **does not execute** the program

What is a static analysis?

Definition: *static analysis* is the systematic examination of an abstraction of program state space

- static analysis **does not execute** the program
 - in contrast to a **dynamic analysis**, such as testing, which does execute the program

What is a static analysis?

Definition: *static analysis* is the systematic examination of an abstraction of program state space

- static analysis **does not execute** the program
 - in contrast to a **dynamic analysis**, such as testing, which does execute the program
- an **abstraction**, in this context, is a **selective representation** of the program that is simpler to analyze

What is a static analysis?

Definition: *static analysis* is the systematic examination of an abstraction of program state space

- static analysis **does not execute** the program
 - in contrast to a **dynamic analysis**, such as testing, which does execute the program
- an **abstraction**, in this context, is a **selective representation** of the program that is simpler to analyze
 - **key idea**: the abstraction will have fewer states to explore
 - hopefully, many fewer!

What is a static analysis?

Definition: *static analysis* is the abstraction of program state space

We have already encountered one kind of static analysis in this class: **type systems**. Type systems aren't special - they are just a very common static analysis.

- static analysis **does not execute**
 - in contrast to a **dynamic analysis**, which executes the program
- an **abstraction**, in this context, is a **selective representation** of the program that is simpler to analyze
 - **key idea**: the abstraction will have fewer states to explore
 - hopefully, many fewer!

Alternative: Dynamic Analysis

Alternative: Dynamic Analysis

- Execute program (over some inputs)
 - The compiler provides the semantics

Alternative: Dynamic Analysis

- Execute program (over some inputs)
 - The compiler provides the semantics
- Observe executions
 - Requires instrumentation infrastructure

Alternative: Dynamic Analysis

- Execute program (over some inputs)
 - The compiler provides the semantics
- Observe executions
 - Requires instrumentation infrastructure
- Analyze results

Alternative: Dynamic Analysis

- Execute program (over some inputs)
 - The **compiler provides the semantics**
- Observe executions
 - Requires instrumentation
- Analyze results

This means that we don't need an **external model** of what the computer does!
(Since your compiler faithfully implements the OpSem, right?)

Dynamic Analysis Properties

Dynamic Analysis Properties

- Can be as **fast** as execution (over a test suite, and allowing for data collection)
 - Example: aliasing

Dynamic Analysis Properties

- Can be as **fast** as execution (over a test suite, and allowing for data collection)
 - Example: aliasing
- **Precise**: no abstraction or approximation

Dynamic Analysis Properties

- Can be as **fast** as execution (over a test suite, and allowing for data collection)
 - Example: aliasing
- **Precise**: no abstraction or approximation
- **Unsound**: results may not generalize to future executions
 - Describes execution environment or test suite

Static Analysis Properties

Static Analysis Properties

- **Slow** to analyze large models of state, so use abstraction

Static Analysis Properties

- **Slow** to analyze large models of state, so use abstraction
- **Conservative**: account for abstracted-away state

Static Analysis Properties

- **Slow** to analyze large models of state, so use abstraction
- **Conservative**: account for abstracted-away state
- **Sound**: (weak) properties are guaranteed to be true
 - Some static analyses are not sound, but static analyses *can* be made sound

Static vs Dynamic Analyses

Dynamic analyses:

Static analyses:

Static vs Dynamic Analyses

Dynamic analyses:

- Concrete execution
 - slow if exhaustive

Static analyses:

- Abstract domain
 - slow if precise

Static vs Dynamic Analyses

Dynamic analyses:

- Concrete execution
 - slow if exhaustive
- **Precise**
 - no approximation

Static analyses:

- Abstract domain
 - slow if precise
- Conservative
 - due to abstraction

Static vs Dynamic Analyses

Dynamic analyses:

- Concrete execution
 - slow if exhaustive
- **Precise**
 - no approximation
- **Unsound**
 - does not generalize

Static analyses:

- Abstract domain
 - slow if precise
- Conservative
 - due to abstraction
- **Sound**
 - due to conservatism

Analogous Analyses

- **Any** analysis problem can be solved with **either** a static or a dynamic analysis

Analogous Analyses

- **Any** analysis problem can be solved with **either** a static or a dynamic analysis
 - e.g., consider **type safety**: no memory corruption or operations on wrong types of values
 - Static type-checking (e.g., Java, Cool)
 - Dynamic type-checking (e.g., Python)

Analogous Analyses

- **Any** analysis problem can be solved with **either** a static or a dynamic analysis
 - e.g., consider **type safety**: no memory corruption or operations on wrong types of values
 - Static type-checking (e.g., Java, Cool)
 - Dynamic type-checking (e.g., Python)
- This insight gives us a kind of “**PL incompleteness theorem**”: either you can know something precisely about one execution (via dynamic analysis) or imprecisely about every execution (via static analysis)

Static vs Dynamic Analyses

Dynamic analyses:

- Concrete execution
 - slow if exhaustive
- Precise
 - no approximation
- **Unsound**
 - does not generalize

Static analyses:

- Abstract domain
 - slow if precise
- **Conservative**
 - due to abstraction
- Sound
 - due to conservatism

Sound Dynamic Analysis?

Sound Dynamic Analysis?

- Observe **every possible execution!**

Sound Dynamic Analysis?

- Observe **every possible execution!**
- Problem: **infinite** number of executions

Sound Dynamic Analysis?

- Observe **every possible execution!**
- Problem: **infinite** number of executions
- Solution: test case selection and generation
 - **Efficiency tweaks** to an algorithm that works perfectly in theory but exhausts resources in practice

Precise Static Analysis?

Precise Static Analysis?

- Reason over **full program state**!

Precise Static Analysis?

- Reason over **full program state**!
- Problem: **infinite** number of executions

Precise Static Analysis?

- Reason over **full program state**!
- Problem: **infinite** number of executions
- Solution: data or execution abstraction
 - **Efficiency tweaks** to an algorithm that works perfectly in theory but exhausts resources in practice

Different Subsets

- Dynamic analysis focuses on a **subset of executions**

Different Subsets

- Dynamic analysis focuses on a **subset of executions**
 - i.e., the executions in the test suite, the executions that random input produces, etc.

Different Subsets

- Dynamic analysis focuses on a **subset of executions**
 - i.e., the executions in the test suite, the executions that random input produces, etc.
 - typically **optimistic** about other executions
 - i.e., assume that they will be bug-free

Different Subsets

- Dynamic analysis focuses on a **subset of executions**
 - i.e., the executions in the test suite, the executions that random input produces, etc.
 - typically **optimistic** about other executions
 - i.e., assume that they will be bug-free
- Static analysis focuses on a **subset of data structures**

Different Subsets

- Dynamic analysis focuses on a **subset of executions**
 - i.e., the executions in the test suite, the executions that random input produces, etc.
 - typically **optimistic** about other executions
 - i.e., assume that they will be bug-free
- Static analysis focuses on a **subset of data structures**
 - more precise for data or control described by the abstraction

Different Subsets

- Dynamic analysis focuses on a **subset of executions**
 - i.e., the executions in the test suite, the executions that random input produces, etc.
 - typically **optimistic** about other executions
 - i.e., assume that they will be bug-free
- Static analysis focuses on a **subset of data structures**
 - more precise for data or control described by the abstraction
 - typically **conservative** / **pessimistic** elsewhere
 - i.e., assume that unmodeled state is unsafe

Next Time On...

- Proper introduction to **one formalism** for static analysis
 - *abstract interpretation* (which I will call “AI” constantly to upset you and Sam Altman)

Next Time On...

- Proper introduction to **one formalism** for static analysis
 - *abstract interpretation* (which I will call “AI” constantly to upset you and Sam Altman)
- Many other formalisms exists

Next Time On...

- Proper introduction to **one formalism** for static analysis
 - *abstract interpretation* (which I will call “AI” constantly to upset you and Sam Altman)
- Many other formalisms exists
 - including **type systems** (which we’ve already discussed)

Next Time On...

- Proper introduction to **one formalism** for static analysis
 - *abstract interpretation* (which I will call “AI” constantly to upset you and Sam Altman)
- Many other formalisms exists
 - including **type systems** (which we’ve already discussed)
 - formally, abstract interpretation is expressive enough that you can describe *any* static analysis using it

Next Time On...

- Proper introduction to **one formalism** for static analysis
 - **abstract interpretation** (which I will call “AI” constantly to upset you and Sam Altman)
- Many other formalisms exists
 - including **type systems** (which we’ve already discussed)
 - formally, abstract interpretation is expressive enough that you can describe *any* static analysis using it
 - that said, you probably don’t want to
 - ask me more about Patrick Cousot’s work in OH

Course Announcements

- **PA2c2** due today
 - if you haven't started yet, you almost certainly won't finish in time (come talk to me about it)
- I'll hold **two short OH today** for those who want to see a test case before PA2c2:
 - right after class (11:25-11:55am)
 - 4:30-5pm
- **PA2 (full)** is due next Monday (one week from today!)

Agenda

- Review: basics of operational semantics
- Operational semantics of Cool
- (if time): introduction to static analysis
 - **further if time: get into abstract interpretation**